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| | | | | Filing Date | November 12, 2003 |
| | | | | First Named Inventor | Kevin T. Look |
| | | | | Art Unit | Unknown |
| | | | | Examiner Name | Unknown |
| Sheet | 2 | of | 2 | Attorney Docket Number | X-1144 US |

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| | | TOYOJI YAMAMOTO et al., "Bias Temperature Instability in Scaled p+ Polysilicon Gate p-MOSFET's," IEEE Transactions on Electron Devices, Vol. 46, No. 5, May 1999, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | N. KIMIZUKA et al., "NBTI enhancement by nitrogen incorporation into ultrathin gate oxide for 0.10-um gate CMOS generation," 2000 Symposium on VLSI Technology Digest of Technical Papers, April 2000, pp. 92-93, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | JEROME B. LASKY et al., "Comparison of Transformation to Low-Resistivity Phase and Agglomeration of TiSi ₂ and CoSi ₂ ," IEEE Transactions on Electron Devices, Vol. 38, No. 2, February 1991, pp. 262-269, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | SE-AUG JANG et al., "Effects of Thermal Processes After Silicidation on the Performance of TiSi ₂ /Polysilicon Gate Device," IEEE Transactions on Electron Devices, Vol. 46, No. 12, December 1999, pp. 2353-2356, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | MOHSEN ALAVI et al., A PROM Element Based on Salicide Agglomeration of Poly Fuses in a CMOS Logic Process, July 1997, pp. 855-858, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
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